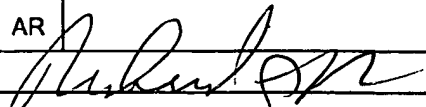
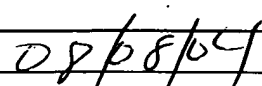


<b>FORM PTO-1449</b> <b>INFORMATION DISCLOSURE STATEMENT</b>				Atty Docket		Application No.	
				MAE 286		To Be Assigned	
				Applicant Toshio NAGATA			
				Filing Date		Group Unit	
				June 27, 2003		To Be Assigned	
<b>U.S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Sub-Class	Translation
AB	AH	08-255905	10/01/96	Japan			Abstract
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>OTHER (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
AB	AO	James C.M. Hwang, "Relationship between gate lag, power drift, and power slump of pseudomorphic high electron mobility transistors", Solid-State Electronics 43 (1999), pp. 1325-1331					
	AP						
	AQ						
	AR						
Examiner				Date Considered			
							
<b>EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							